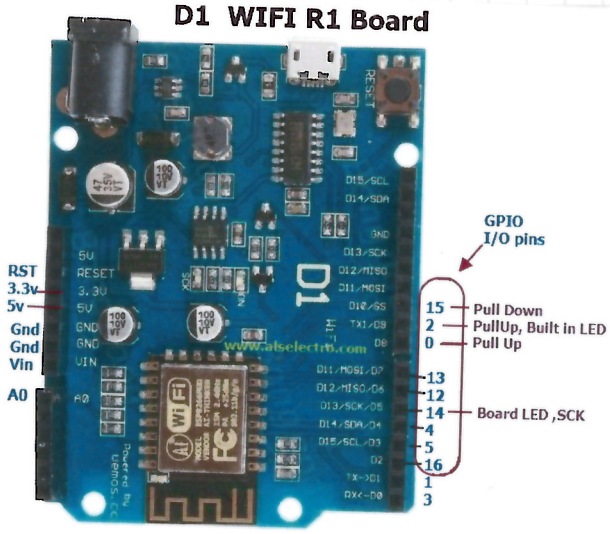
22/1/2019

HLW 8012

Sel ø CF1 current

Sel 1 CF1 voltage

23/1/2019




```
#define SEL_PIN  5 //wemosD1 GPIO5 D3
#define CF1_PIN 13 //wemosD1 GPIO5 D7
#define CF_PIN 14 //wemosD1 GPIO5 D5
// Set SEL_PIN to HIGH to sample current
// This is the case for Itead's Sonoff POW, where a
// the SEL_PIN drives a transistor that pulls down
// the SEL pin in the HLW8012 when closed
#define CURRENT_MODE                    HIGH
```

ESP_Easy_mega-20190121_hard_core_250_beta_SONOFF_POW_4M.bin

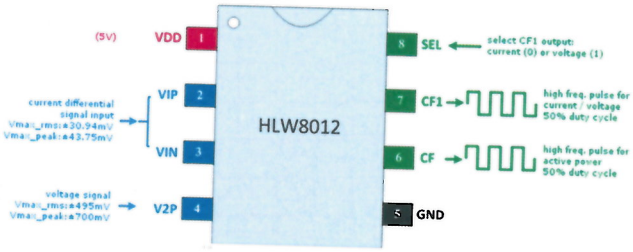
$$F_{CF}=\frac{V1\times V2\times 48}{V_{REF}^2}\times\frac{f_{osc}}{128}$$

f_{osc}: 3.579MHz

V_{REF}: 2.43V

$$F_{CFI}=\frac{V1\times 24}{V_{REF}}\times\frac{f_{osc}}{512}$$

$$F_{CFU}=\frac{V2\times 2}{V_{REF}}\times\frac{f_{osc}}{512}$$



V2P VIN Vmaxims 495mV Vpp 700mV

VIP VIN Vmaxims 30,94mV Vpp 43,75mV

⚠ CSE 7766 version numerique (UART) du HLW8012

chipsea.com

///

CSE 7759B UART VS CSE 7759

HLW8032 ≡ CSE 7759B

hiliwi.com

nouvelle version 2018 HLW 8110